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CONFIRMATION NO. 2525

Bib Data Sheet

|   |   |                               |  |                                    |                                |
|---|---|-------------------------------|--|------------------------------------|--------------------------------|
| SERIAL NUMBER<br>09/919,372   | FILING DATE<br>07/31/2001<br><br>RULE   | CLASS<br>714                  | GROUP ART UNIT<br>2133   | ATTORNEY<br>DOCKET NO.<br>5347-205 |                                |
| <b>APPLICANTS</b><br><br>Vadim Gutnik, Austin, TX;<br><br>Anantha Chandrakasan, Belmont, MA;  |   |                               |  |                                    |                                |
| <b>** CONTINUING DATA *****</b> <i>yes me</i><br>This appln claims benefit of 60/221,709 07/31/2000   |   |                               |  |                                    |                                |
| <b>** FOREIGN APPLICATIONS *****</b> <i>None me</i>   |   |                               |  |                                    |                                |
| <b>IF REQUIRED, FOREIGN FILING LICENSE GRANTED</b><br><b>** 09/07/2001</b>  |   |                               |  |                                    |                                |
| Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no<br>35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after<br>met Allowance<br>Verified and <i>me</i><br>Acknowledged Examiner's Signature Initials |   | STATE OR<br><br>COUNTRY<br>TX | SHEETS<br><br>DRAWING<br>4   | TOTAL<br><br>CLAIMS<br>26          | INDEPENDENT<br><br>CLAIMS<br>6 |
| <b>ADDRESS</b><br>20792<br>MYERS BIGEL SIBLEY & SAJOVEC<br>PO BOX 37428<br>RALEIGH, NC<br>27627   |   |                               |  |                                    |                                |
| <b>TITLE</b><br>Clock distribution circuits and methods of operating same that use multiple clock circuits connected by phase detector circuits to generate and synchronize local clock signals   |   |                               |  |                                    |                                |
| FILING FEE  | FEES: Authority has been given in Paper<br>No. _____ to charge/credit DEPOSIT ACCOUNT<br>No. _____ for following: |                               | <input type="checkbox"/> All Fees<br><input type="checkbox"/> 1.16 Fees ( Filing )<br><input type="checkbox"/> 1.17 Fees ( Processing Ext. of time.) |                                    |                                |
| RECEIVED  |   |                               |  |                                    |                                |